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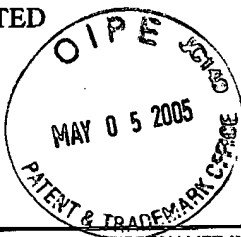
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23494 7590 04/19/2005

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| | |
|--------------------|--------------------|
| Karen Vertz | (Depositor's name) |
| <i>Karen Vertz</i> | (Signature) |
| 5-5-05 | (Date) |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
|-----------------|-------------|----------------------|---------------------|------------------|

10/725,642

12/02/2003

Taylor R. Eiland

TI-32451

4541

TITLE OF INVENTION: LOW COST FABRICATION METHOD FOR HIGH VOLTAGE, HIGH DRAIN CURRENT MOS TRANSISTOR

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 07/19/2005 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|--------------|----------|----------------|
| PERT, EVAN T | 2826 | 438-286000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 **W. James Brady, III**

2 **Frederick J. Telecky, Jr.**

3

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Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, Texas

05/06/2005 AHONDAF2 00000074 200668 10725642

01 FC:1501 1400.00 DA

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Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Gary C. Honeycutt

Typed or printed name

Date

5 May '05

Registration No.

20,250

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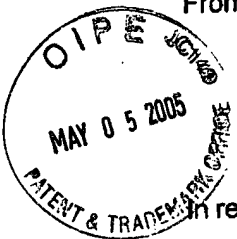
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant

Taylor R. Efland, et al.

Docket Number: TI-32451

Serial No.: 10/725,642

Art Unit: 2826

Filed: 12/02/03

Examiner: Evan T. Pert

For: Low Cost Fabrication Method for High Voltage, High Drain
Current MOS Transistor

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile
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Karen Vertz
Karen Vertz

5-5-05
Date

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| NAME OF INVENTOR(S): Taylor R. Efland, et al. | |
| TITLE OF INVENTION: Low Cost Fabrication Method for High Voltage, High Drain Current MOS Transistor | |
| TI FILE NO.: TI-32451 | DEPOSIT ACCT. NO.: 20-0868 |
| FAXED: 5-6-05 DUE: 7-19-05 ATTY/SECY: GCH/kjv | |
| RECEIPT DATE & SERIAL NO.: Serial No.: 10/725,642 Filing Date: 12/02/03 | |

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